SESHADRI RAO GUDLAVALLERU ENGINEERING COLLEGE

(An Autonomous Institute with Permanent Affiliation to JNTUK, Kakinada) Seshadri Rao Knowledge Village, Gudlavalleru

Office of the Controller of Examinations M.Tech II Semester (R20) Regular Examinations, September 2023

TIME TABLE

Timings: 01:30 p.m. to 04:30 p.m.

31st August 2023

Specialization	07.09.2023	11.09.2023	13.09.2023	15.09.2023	19.09.2023
	(Thursday)	(Monday)	(Wednesday)	(Friday)	(Tuesday)
VLSI Design & Embedded Systems	VLSI System Design (EC4911)	Embedded System Based IoT (EC4912)	System on Chip Design (EC4915)	Communication Buses and Interfaces (EC4917)	English for Research Paper Writing (EG3901)

Note: Seating arrangement will be notified on the day of each examination half an hour before the commencement of the examination.

Controller of Examinations

Principal

Copy to: HODs – ECE/ENG for Notice Board display/Exam Section (Autonomous)

Tables of Principal/Vice-Principal (Academics)/Vice-Principal (Administration)